IN THE CLAIMS:

Please amend the claims as shown below. The claims, as pending in the subject application, read as follows:

 (Currently Amended) An information processing apparatus comprising: an interface arranged to connect to a serial bus compatible to or complying with the IEEE 1394 standard; and

a memory arranged to store, in a predetermined <u>first</u> address area, configuration ROM information complying with the IEEE 1212 standard, and to store configuration information identical to the configuration ROM information in <u>an a second</u> address area different from the predetermined <u>first</u> address area.

- (Currently Amended) The apparatus according to claim 1, wherein the configuration ROM information contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 3. (Currently Amended) The apparatus according to claim 1, wherein the configuration ROM information has a general format defined in the IEEE 1212 standard, and the first entry in the root directory of the configuration ROM information contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 4. (Currently Amended) The apparatus according to claim 1, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.

5. (Currently Amended) An information processing apparatus comprising: an interface arranged to connect to a serial bus compatible to or complying with the IEEE 1394 standard; and

a memory arranged to store, in a predetermined <u>first</u> address area, configuration ROM information with a minimal format complying with the IEEE 1212 standard, and to store configuration information corresponding to <u>the</u> configuration ROM information with a general format complying with the IEEE 1212 standard in <u>an</u> a second address area different from the predetermined <u>first</u> address area.

- 6. (Currently Amended) The apparatus according to claim 5, wherein the configuration ROM information with the minimal format contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 7. (Currently Amended) The apparatus according to claim 5, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.
- 8. (Currently Amended) An information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM information complying with the IEEE 1212 standard, which is stored in a predetermined <u>first</u> address area of a device; and

reading out configuration information identical to the configuration ROM information, which is stored in an a second address area different from the predetermined first address area, on the basis of the readout information.

- 9. (Currently Amended) The method according to claim 8, wherein the configuration ROM information contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 10. (Currently Amended) The method according to claim 8, wherein the configuration ROM information has a general format defined in the IEEE 1212 standard, and the first entry in the root directory of the configuration ROM information contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 11. (Currently Amended) The method according to claim 8, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.
- 12. (Currently Amended) An information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising the steps of:

reading out at least part of configuration ROM information with a minimal format complying with the IEEE 1212 standard, which is stored in a predetermined <u>first</u> address area of a device; and

reading out <u>configuration</u> information corresponding to <u>the</u> configuration ROM information with a general format according to the IEEE 1212 format, which is stored in <u>an a second</u> address area different from the predetermined <u>first</u> address area, on the basis of the readout information.

- 13. (Currently Amended) The method according to claim 12, wherein the configuration ROM information contains information used to refer to the <u>second</u> address area where the configuration information is stored.
- 14. (Currently Amended) The method according to claim 12, wherein the second address area where the configuration information is to be stored is an area where a block read transaction with a large block size is supported.
- 15. (Currently Amended) A computer program product comprising a computer readable medium storing a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising process procedure code for:

reading out at least part of configuration ROM information complying with the IEEE 1212 standard, which is stored in a predetermined <u>first</u> address area of a device; and

reading out configuration information identical to the configuration ROM information, which is stored in an a second address area different from the predetermined first address area, on the basis of the readout information.

16. (Currently Amended) A computer program product comprising a computer readable medium storing a computer program code, for an information processing method of processing information between a plurality of devices connected to a serial bus compatible to or complying with the IEEE 1394 standard, comprising process procedure code for:

reading out at least part of configuration ROM information with a minimal format complying with the IEEE 1212 standard, which is stored in a predetermined <u>first</u> address area of a device; and

reading out <u>configuration</u> information corresponding to <u>the</u> configuration ROM information with a general format complying with the IEEE 1212 format, which is stored in <u>an a second</u> address area different from the predetermined <u>first</u> address area, on the basis of the readout information.